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HAMRE, SCHUMANN, MUELLER & LARSON, P.C.
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Antonette Peters

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Antonette Peters

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January 23, 2006

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,302	12/01/2003	Naoki Kuroda	10873.1353US01	3100

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$0	\$0	\$0	03/20/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
ZWEIZIG, JEFFERY SHAWN	2816	327-408000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

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1 Hamre, Schumann,

2 Mueller & Larson, P.C.

3

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Matsushita Electric Industrial Co., Ltd.

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Osaka, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Date January 23, 2006

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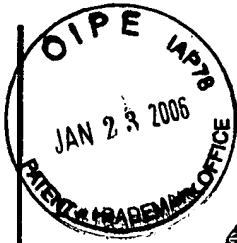
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FAX TRANSMISSION January 23, 2006

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Examiner: ZWEIZIG
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FROM: Douglas P. Mueller

OUR REF: 10873.1353US01

TELEPHONE: (612) 455.3800

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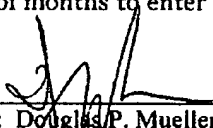
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Title of Document: **Issue Fee Transmittal - Part B**


Applicant: KURODA
Serial No.: 10/726,302
App. Filed: December 1, 2003
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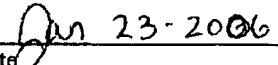
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